Serialise bits ; implement single charater serilisation at single rate : test bench

Serialise bytes ; implement state machine that feeds bytes into RS232 module (test bench to verify )

Serialise bytes ; implement serial stream where one or more bytes chance each cycle (e.g cycles)

Serialise bits ; implement multi character serial stream (fixed data)

Create serial stream that follows RS232 protocol and timing on the sending of bytes (test bench to verify )

Create serial stream of custom [9 byte] header to PC

Implement state machine where states are changed depending on external inputs from ADC ( timing counters included

Implement behavioral timer with combinational logic (e.g CS & RD pins low trigger behavioral timer)

Implement 4 state machine and create testbench that fully cycles through 4 states and verify wave form for input and out flag pins

Implement state machine with behavioral counters to model required ADC control timing

Implement behavioral timers, after counting , change control flag-bit; change of states occurs

State machine [ state are : initialising ,Idle , convert(analogue to digital data) , read (digital output)]

Control State machine [ func : start conversion + Read data]

Serial communication

ADC control-communication